Timing Recovery

Prof. David Johns University of Toronto

(johns@eecg.toronto.edu) (www.eecg.toronto.edu/~johns)



slide 1 of 18

© D.A. Johns, 1997



Deductive Timing Recovery

- Non-linear spectral line method most popular (linear spectral line method used if *f_s* tone present).
- Apply a non-linearity to receive signal and bandpass filter to recover f_s tone (usually with PLL).
- Works because receive signal is cyclostationary (i.e. its moments vary in time and are periodic).
- Common non-linearities used are squaring and absolute circuit (rectifier) (for low excess BW)
- Ensemble average of non-linear circuit output is periodic in T
- Thus, a *f_s* component exists (scrambled data)



slide 3 of 18





Deductive Timing

 Can pre-filter receive signal to only non-flat portion to reduce jitter — eliminate portion that does not contribute to timing tone.



Inductive Timing — Early Late

• Can sample at 2X and determine if clock is early or late when a transition occurs.



- If (a) = (b) = (c), do nothing
- However, (b) sample does not indicate how far away from zero crossing — can add dither to (b) to aid estimate.



slide 7 of 18

- Commonly realized as minimum mean-square error (i.e. MMSE timing)
- Also called LMS timing.
- Assume sample times are $kT + \tau_k$





slide 8 of 18

• MMSE adjusts τ_k to minimize

$$E[E_{k}^{2}(\tau_{k})] = E[(Q_{k}(\tau_{k}) - A_{k})^{2}]$$
(1)

where $E[\bullet]$ denotes expectation, $Q_k(\tau_k)$ is the sampled signal (it is a function of τ_k) and A_k is the ideal symbol.

• Stochastic gradient (as in LMS algorithm) leads to

$$\tau_{k+1} = \tau_k - \mu \left(E_k(\tau_k) \times \frac{\partial Q_k(\tau_k)}{\partial \tau_k} \right)$$
(2)



slide 9 of 18

Can replace derivative wrt τ_k by derivative wrt time since sampled at t = kT + τ_k

$$\frac{\partial Q_k(\tau_k)}{\partial \tau_k} = \left. \frac{\partial Q(t)}{\partial t} \right|_{t = kT + \tau_k}$$
(3)



<u>ط</u>

TT

University of Toronto

slide 10 of 18

• Can sample at 2X symbol-rate and perform derivative in discrete-time.



2X Timing Example

• Sample at twice symbol-rate

$$\tau_{k+1} = \tau_k - \mu(Q_k - A_k) \times (Q_{k+1} - Q_{k-1})$$
(4)



- At Q_k , slope is neg, E_k is neg, so τ_k is decreased.
- · Use absolute values then 50% duty cycle not needed



slide 12 of 18

 If all sampling done at symbol-rate, MMSE timing can still be used — base it on impulse response.



- Early-late adjust so $h_1 h_{-1} = 0$
- Zero-crossing adjust so $h_1 = 0$

University of Toronto

Ť

slide 13 of 18

- To obtain impulse response estimates, cross correlate received signals with received symbols.
- Recall

$$Q(t) = \sum_{m} A_{m} h(t - mT) + n(t)$$
(5)

• Sampled at time $kT + \tau$, we have

$$Q_k \equiv Q(kT + \tau)$$

$$= \dots + A_{k-1}h(kT + \tau - (k-1)T) + A_kh(kT + \tau - kT) + \dots$$

$$= \dots + A_{k-1}h_1(\tau) + A_kh_0(\tau) + A_{k+1}h_{-1}(\tau) + \dots$$
(6)

where $h_k(\tau) \equiv h(kT + \tau)$

University of Toronto

slide 14 of 18

© D.A. Johns, 1997

- To estimate $h_1(\tau)$, use $Q_k \times A_{k-1}$
- All other terms go to zero since A_{k-1} is uncorrelated with A_j when $k \neq j$
- To estimate $h_{-1}(\tau)$, we need to use a delayed version of Q_k

$$Q_{k-1} = \dots + A_{k-1}h_0(\tau) + A_kh_{-1}(\tau) + A_{k+1}h_{-2}(\tau) + \dots$$
(7)

• To estimate $h_{-1}(\tau)$, use $Q_{k-1} \times A_k$



slide 15 of 18

• To build early-late scheme,



- Early-late is insensitive to amplitude distortion.
- Zero-crossing is better where phase distortion dominates
- *h*₀ factor should be known otherwise adaptation gain will vary (can divide it out in algorithm).



University of Toronto

slide 16 of 18

A Fractional-N Frequency Synthesizer

- Often need a low jitter clock that can have arbitrary frequency.
- A voltage-controlled crystal oscillator is expensive.
- Use oversampling within a PLL



Elastic Buffer

- Used to deal with low frequency input clock jitter
- Allows attenuation of clock jitter to next stage

Example

- Input clock rate 1MHz but varies from 0.9MHz to 1.1MHz in sinusoidal fashion at 1kHz
- Output clock rate fixed at 1MHz
- Input clock high 16 extra bits stored in buffer
- Input clock low 16 bits removed from buffer
- Keep elastic buffer half-full on-average through feedback



slide 18 of 18