

Sponsor presentation (Intel): 10:30 am - 11:00 am

Jonathan Rogers: Application and design challenges for high speed SERDES IP

Discussion of key applications and design challenges for high speed SERDES IP in advanced process nodes.

Bio ★ Jonathan Rogers

Jon Rogers graduated in 2001 with a Masters of Applied Science focused on the design and implementation of 10Gbs CDR in 0.18um CMOS. He has held high-speed mixed signal design roles at Inphi and Snowbush Microelectronics as well as design leadership roles at Snowbush, Gennum, VSEMI and Intel where he is currently the Director of Engineering for the SERDES development organization within Intel Custom Foundry.

