



TECH TALK
INTEL

“Intel and Intel Custom Foundry SerDes Overview”

Dr. Aleksey Tyshchenko (Senior Systems Engineer, Intel)

Speaker Bio

Aleksey Tyshchenko received his B.A.Sc. degree in 2004 from the Division of Engineering Science, University of Toronto, and M.A.Sc. and Ph.D. degrees in 2006 and 2011 respectively from the Department of Electrical and Computer Engineering, University of Toronto. His Ph.D. research focused on the design of clock and data recovery (CDR) systems for high-speed ADC-based receivers. In 2006, he spent 6 months with Fujitsu Labs of America working on circuit design for high-speed signaling applications. In 2011 he joined V Semiconductor as an analog designer, and since 2012 he has been with Intel working as a senior systems engineer on various aspects of high-speed SerDes IP development.