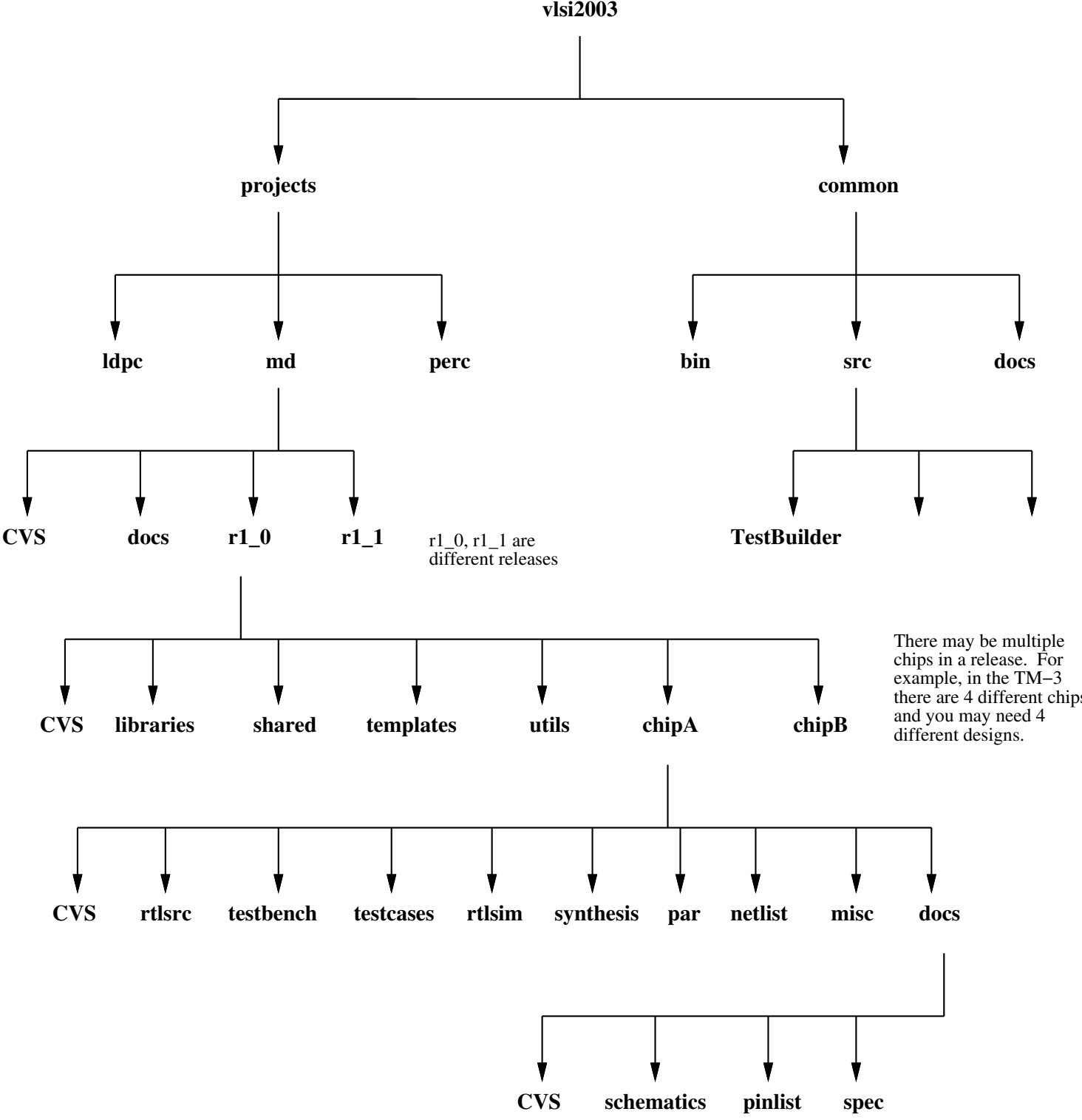
vlsi2003
projects
common
ldpc
md
perc
bin
src
docs
CVS
docs
r1_0
r1_1
r1_0, r1_1 are different releases
TestBuilder
CVS
libraries
shared
templates
utils
chipA
chipB
There may be multiple chips in a release. For example, in the TM−3 there are 4 different chips and you may need 4 different designs.
CVS
rtlsrc
testbench
testcases
rtlsim
synthesis
par
netlist
misc
docs
CVS
schematics
pinlist
spec