ECE 1373S—2003
PROGRESS REPORT

Project:

Report Number:

Date:

Goals for the Week:

Goals Achieved:

Goals Not Achieved: (Why? List Problems Encountered)

Describe what (if anything) **was done in the following areas, and the approximate time spent** (hours).

Understanding the Problem (i.e., what to build):

Algorithmic (C/C++) Simulation:

Learning Time (Looking in manuals, figuring how to do something):

Tool Debugging (You think you know what to do, but it doesn't work):

Tool Development (Writing new tools, scripts):

Logic Design:

Schematic Entry:

RTL Coding:

RTL Simulation:

Gate-Level Simulation:

Spice Simulation:

Synthesis:

Floorplanning:

Place and Route:

Extraction (for Back-annotation):

Test Circuit Generation:

Test Vector Generation:

Testbench Coding:

Test Case Coding:

Verification (Running Test Cases and Debugging):

Please add other categories, if none of the above fit.

More copies may be found at

http://www.eecg.toronto.edu/~pc/courses/vlsi/2003/

The files are called *progress.ps* and *progress.tex* for the source.