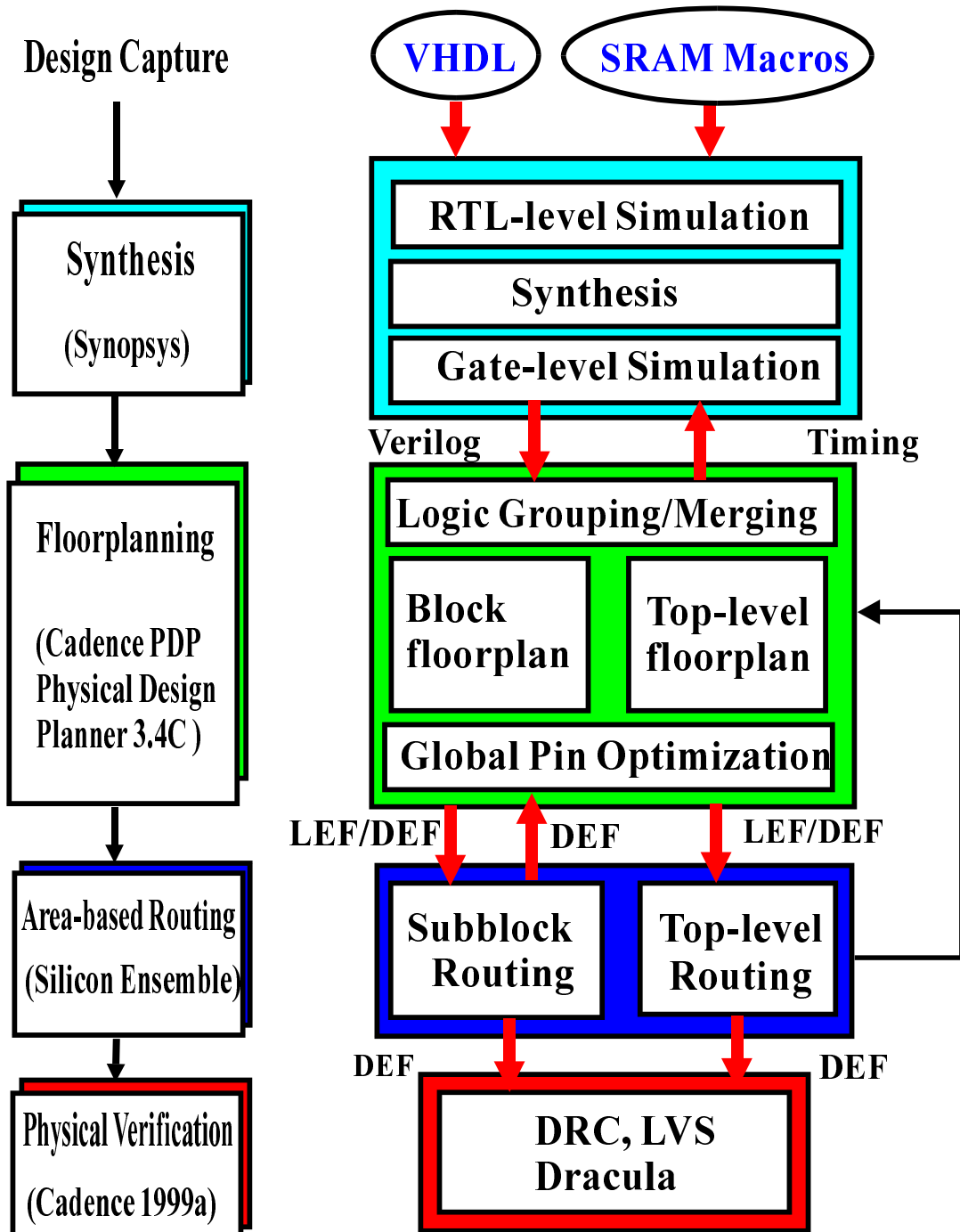


# CAD Flow Overview

- by Sean



## Topics Covered:

1. Writing good VHDL code
2. Correctly use DesignWare library
3. Using Simulation Control Language
4. Describing the timing constraints
5. Writing scripts to synthesize a chip
6. Correctly synthesize DesignWare components (various implementations)
7. Simple exercise
  - Hierarchical Design
  - Synthesize the chip
  - Follow CMC DSM flow
  - What if you have a big chip !
8. Top-down Hierarchical P&R flow
9. Bottom-up Hierarchical P&R flow
10. Using Physical Design Planner to do the job !
11. Area-based Routing -- DEF/LEF editing
12. Power Planning -- Stripes and Rings
13. Clock-Tree-Synthesis
14. Static Timing Analysis
15. DRC editing / Metal filling