Crosstalk-Aware Transmitter Pulse-Shaping for Parallel Chip-to-Chip Links

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Board-to-Board Channel





Characterize the Channel



Impulse and Frequency Responses





Conventional Solutions



⁽many links in parallel)

slew-rate limiting

- pro: simple
- con: not good when ISI is severe
- crosstalk cancellation in addition to transmit filter G
 - pro: good performance
 - con: hardware cost

Example: Different Pulse Shapes



Example: Different Pulse Shapes



Example: Different Pulse Shapes



Optimize with Respect to a Figure of Merit





meFind E2C for each Pulse Shape



•
$$E2C = \frac{485 \, mV}{2 \times 157 \, mV} = 1.54$$

Repeat calculation for all candidate pulse shapes

Maximum E2C for Various Filter Types at 2.7 Gb/s



Increasing Total Taps



Increasing Granularity



Increasing Taps per UI



Hardware Proof-of-Concept







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Crosstalk-Aware Equalization Over Chip-to-Chip Links

Channel Introduces ISI and Crosstalk

2.7 Gb/s
PRBS: 2³¹–1





- input to channel
- square pulse

- output from channel
- no aggressors

- output from channel
- two aggressors

Filter Opens the Eye



- output from channel
- two aggressors
- square pulse input
- jitter_{RMS} = 53 ps



- pulse shape chosen to maximize E2C
- 3 total taps
- 2 taps per UI
- jitter_{RMS} = 33 ps

Filter Improves Bit Error Rate



- square pulse: $BER = 10^{-5}$
- crosstalk-aware pulse: BER < 10⁻¹²

- pre-emphasis pulse: $BER = 10^{-7}$
- crosstalk-aware pulse: $BER = 10^{-5}$

- Crosstalk is significant in board-to-board channels
- Received eye opening can be increased by taking crosstalk into account when equalizing
- Crosstalk-aware pulse shape decreased BER by 10² at 2.7 Gb/s