1. Modify the gencode algorithm (Fig. 9.25) for a target machine whose arithmetic operation instructions have three operands ALL in registers. This means that an arithmetic instruction looks like this:

```
    op result argument1 argument2
```

where `result`, `argument1`, `argument2` are in registers.

Use your modified algorithm to generate code for the tree in fig. 9.18.

2. Problem 9.14, p.582