



How Can CMC Microsystems Help You Achieve
Your Microsystems Research Objectives?

Learn About the Test Capability Available through the
National Microelectronics and Photonics Testing Collaboratory

Research Ready – An Update on the National Microelectronics and
Photonics Testing Collaboratory

Dr. Robert Mallard, Manager for Hybrid Packaging and Microsystems Test
CMC Microsystems

Connections 2007 ECE Graduate Student Symposium

Friday, June 15, 2007 at 12 Noon

Bahen Centre for Information Technology

40 St. George St.

University of Toronto, Toronto, Ontario

Does your research project include requirements for testing and validating high-performance microelectronic and photonic concepts? Learn how the wealth of test and measurement resources available through the National Microelectronics and Photonics Testing Collaboratory can help you achieve your research objectives. Join CMC Microsystems (www.cmc.ca) for a presentation at Connections 2007 at 12 Noon on June 15, 2007, at the University of Toronto, Toronto, Ontario.

The National Microelectronics and Photonics Testing Collaboratory is a Canada-wide initiative that brings world-class test capability, tools and technologies online for graduate students, faculty members and researchers at Canadian universities.

Managed by CMC Microsystems, the 'virtual laboratory' facilitates both onsite and remote access to state-of-the-art, industry-grade equipment located at four specialized labs. These labs specialize in the verification and test of digital microelectronic technology, mixed-signal systems, RF components and photonic systems.

You will hear directly from CMC's test engineers about:

- * The Testing Collaboratory and the capability available to you
- * How you can access this capability
- * How other university researchers have benefited from the Collaboratory
- * The opportunity to participate in a remote test of your device or system

For more information please visit us at www.cmc.ca/test or contact Robert Mallard, Engineering Manager, Hybrid Packaging and Microsystems Test at rob_mallard@cmc.ca