Board and Design Tools Request Form

ECE532S Digital Systems Design

Due January 13, 2010 in lab

1 Xilinx Tools

You will be provided with an install key for the Xilinx tools upon submission of this form with your signatures on the second page.

2 Board Pickup

You must bring your own boards to the lab.

Starting Monday, January 11, you can get pick up your boards from Norm in BA3110 or Fred in BA3104. They are closed 12-1 for lunch and leave at 4pm. You will need to provide the Name, Student Number, and email address for all members of your group.

3 Board Information

All groups will use the Xilinx XUP Virtex II Pro development board. More information about the board can be found at http://www.eecg.toronto.edu/~pc/courses/edk/

You will be expected to work in groups of three.

At this point, the actual project you will be doing has not been approved so answer these questions based on what you are currently thinking about.

Give a short description of what you are thinking about for your project in the space below. You will not be held to this idea. It only gets you thinking and gives us an idea of what might be needed.

Answer the following questions:

- 1. Will you need to connect circuitry directly to the FPGA? Circle one: yes no
- 2. Do you need to run Linux on the microprocessor in your design? Circle one: yes no
- 3. Which lab period will you attend? Circle: 12 noon 4pm

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4 Design Tools Signout

This section is to ensure that you understand the conditions for use of the software.

The software provided to you in this course is for course work only. The software is for your own use and not to be redistributed. You will be provided with two DVDs with the tools distribution and a service pack. Instructions will be given out when this form is received.

Any violations will be considered an academic offense.

All members of the group must sign below and agree to these conditions before the group will be given the install key.

Member 1:	
Print Name:	Student Number:
Email address:	
Signature:	Date:
Member 2:	
Print Name:	Student Number:
Email address:	
Signature:	Date
512haute	Dutt
Member 3:	
Print Name:	Student Number:
Email address:	
Signature:	Date: